



## UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei YAMAZAKI et al. Art Unit : 2826  
Serial No. : 09/773,543 Examiner : Victor A. Mandala, Jr.  
Filed : February 2, 2001 Confirmation No.: 8040  
Notice of Allowance Date: September 24, 2004  
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE  
SAME

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF ALLOWANCE**

In response to the Notice of Allowance mailed September 24, 2004, enclosed are a completed issue fee transmittal form PTOL-85b and authorization to charge Deposit Account No. 06-1050 in the amount of \$1730 for the required issue fee and publication fee, including patent copies.

It is noted that the allowed claims listed on the notice of allowability are erroneously identified as claims 1-51. In a response to restriction requirement filed on September 12, 2002, non-elected claims 38-44 were canceled. It is respectfully requested that a supplemental notice of allowability be issued to show the correction, i.e., allowed claims 1-37 and 45-51.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

  
John F. Hayden  
Reg. No. 37,640

Date: December 23, 2004

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